

MicroModule (M.2) Pinout v1.0

An Input is into the module. The module controls Outputs.

		GND	75
74	3.3V	G5 / BUS5	73
72	RTC_3V_BATT	G6 / BUS6	71
70	SPI_CS1# / SDIO_DATA3 (I/O)	G7 / BUS7	69
68	SDIO_DATA2 (I/O)	G8	67
66	SDIO_DATA1 (I/O)	G9 / ADC_D- / CAM_HSYNC	65
64	SPI_CIPO1 / SDIO_DATA0 (I/O)	G10 / ADC_D+ / CAM_VSYNC	63
62	SPI_COPI1 / SDIO_CMD (I/O)	SPI_CIPO (I)	61
60	SPI_SCK1 / SDIO_CLK (O)	SPI_COPI (O)	59
58	AUD_MCLK (O)	SPI_SCK (O)	57
56	AUD_OUT / PCM_OUT / I2S_OUT / CAM_MCLK	SPI_CS#	55
54	AUD_IN / PCM_IN / I2S_IN / CAM_PCLK	I2C_SCL1 (I/O)	53
52	AUD_LRCLK / PCM_SYNC / I2S_WS / PDM_DATA (I/O)	I2C_SDA1 (I/O)	51
50	AUD_BCLK / PCM_CLK / I2S_SCK / PDM_CLK (I/O)	BATT_VIN/3 (I - ADC) (0/3.3V)	49
48	G4 / BUS4	PWM1	47
46	G3 / BUS3	GND	45
44	G2 / BUS2	CAN_TX	43
42	G1 / BUS1	CAN_RX	41
40	G0 / BUS0	GND	39
38	A1	USBHOST_D-	37
36	GND	USBHOST_D+	35
34	A0	GND	33
32	PWM0	Module Key	31
30	Module Key	Module Key	29
28	Module Key	Module Key	27
26	Module Key	Module Key	25
24	Module Key	Module Key	23
22	UART_TX2 (O)	SWDCK	21
20	UART_RX2 (I)	UART_RX1 (I)	19
18	D1 / CAM_TRIG	UART_TX1 (O)	17
16	I2C_INT# (I)	UART_CTS1 (I)	15
14	I2C_SCL (I/O)	UART_RTS1 (O)	13
12	I2C_SDA (I/O)	BOOT (I - Open Drain)	11
10	D0	USB_VIN	9
8	G11 / SWDSWO	GND	7
6	RESET# (I - Open Drain)	USB_D-	5
4	3.3V_EN	USB_D+	3
2	3.3V	GND	1

Notes:

All pins are 3.3V compatible unless otherwise specified. Don't expose any 3.3V pins to 5V.

Each MicroMod Processor Board has a GPIO controlled LED on the module not exposed to the board edge (aka LED_BUILTIN).

A0/A1 should be assigned to pins that are exclusively ADC (no PWM capability)

PWM0/PWM1 should be assigned to pins that are exclusively PWM (no ADC capability)

D0/D1 should be assigned to pins that are exclusively GPIO (no ADC or PWM capability)

If the microcontroller lacks a specific pin function, and has left over GPIO, they can be over-ruled with

GPIO. For example CTS/RTS can be overwritten with a GPIO if the microcontroller does not have flow control.