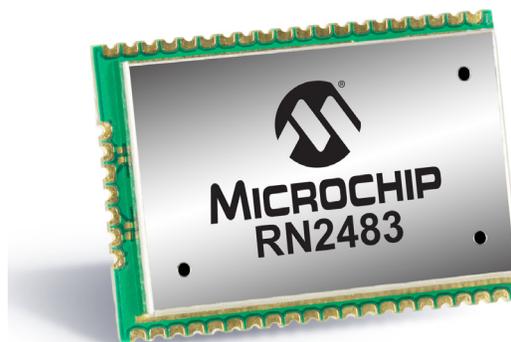

Low-Power Long Range LoRa™ Technology Transceiver Module

General Features

- On-board LoRaWAN™ Class A protocol stack
- ASCII command interface over UART
- Compact form factor: 17.8 x 26.7 x 3 mm
- Castellated SMT pads for easy and reliable PCB mounting
- Environmentally friendly, RoHS compliant
- European R&TTE Directive Assessed Radio Module
- Device Firmware Upgrade (DFU) over UART, see “RN2483 LoRa™ Technology Module Command Reference User’s Guide” (DS40001784A)



Operational

- Single operating voltage: 2.1V to 3.6V (3.3V typical)
- Temperature range: -40°C to +85°C
- Low-power consumption
- Programmable RF Communication Bit Rate up to 300 kbps with FSK modulation, 5468 bps with LoRa™ Technology modulation
- Integrated MCU, Crystal, EU1-64 Node Identity Serial EEPROM, Radio Transceiver with Analog Front End, Matching Circuitry
- 14 GPIOs for control and status

RF/Analog Features

- Low-Power Long Range Transceiver operating in the 433 MHz and 868 MHz frequency bands
- High Receiver Sensitivity: down to -148 dBm
- TX Power: adjustable up to +14 dBm high efficiency PA
- FSK, GFSK, and LoRa Technology modulation
- IIP3 = -11 dBm
- >15 km coverage at suburban and >5 km coverage at urban area

Description

Microchip’s RN2483 Low-Power Long Range LoRa Technology Transceiver module provides an easy to use, low-power solution for long range wireless data transmission. The advanced command interface offers rapid time to market.

The RN2483 module complies with the LoRaWAN Class A protocol specifications. It integrates RF, a baseband controller, command Application Programming Interface (API) processor, making it a complete long range Solution.

The RN2483 module is suitable for simple long range sensor applications with external host MCU.

Applications

- Automated Meter Reading
- Home and Building Automation
- Wireless Alarm and Security Systems
- Industrial Monitoring and Control
- Machine to Machine
- Internet of Things (IoT)

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An errata sheet, describing minor operational differences from the data sheet and recommended workarounds, may exist for current devices. As device/documentation issues become known to us, we will publish an errata sheet. The errata will specify the revision of silicon and revision of document to which it applies.

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- Your local Microchip sales office (see last page)

When contacting a sales office, please specify which device, revision of silicon and data sheet (include literature number) you are using.

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1.0 DEVICE OVERVIEW

The RN2483 Transceiver module features LoRa Technology RF modulation, which provides long range spread spectrum communication with high interference immunity.

Using LoRa Technology modulation technique, RN2483 can achieve a receiver sensitivity of -148 dBm. The high sensitivity combined with the integrated +14 dBm power amplifier yields industry leading link budget, which makes it optimal for applications requiring extended range and robustness.

LoRa Technology modulation also provides significant advantages in both blocking and selectivity compared to the conventional modulation techniques, solving the traditional design compromise between extended range, interference immunity, and low-power consumption.

The RN2483 module delivers exceptional phase noise, selectivity, receiver linearity, and IIP3 for significantly lower power consumption. [Figure 1-1](#), [Figure 1-2](#), and [Figure 1-3](#) show the module's top view, the pinout, and the block diagram.

FIGURE 1-1: RN2483 TOP VIEW

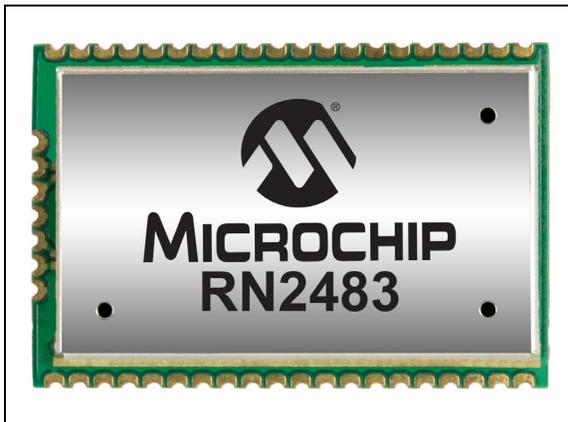
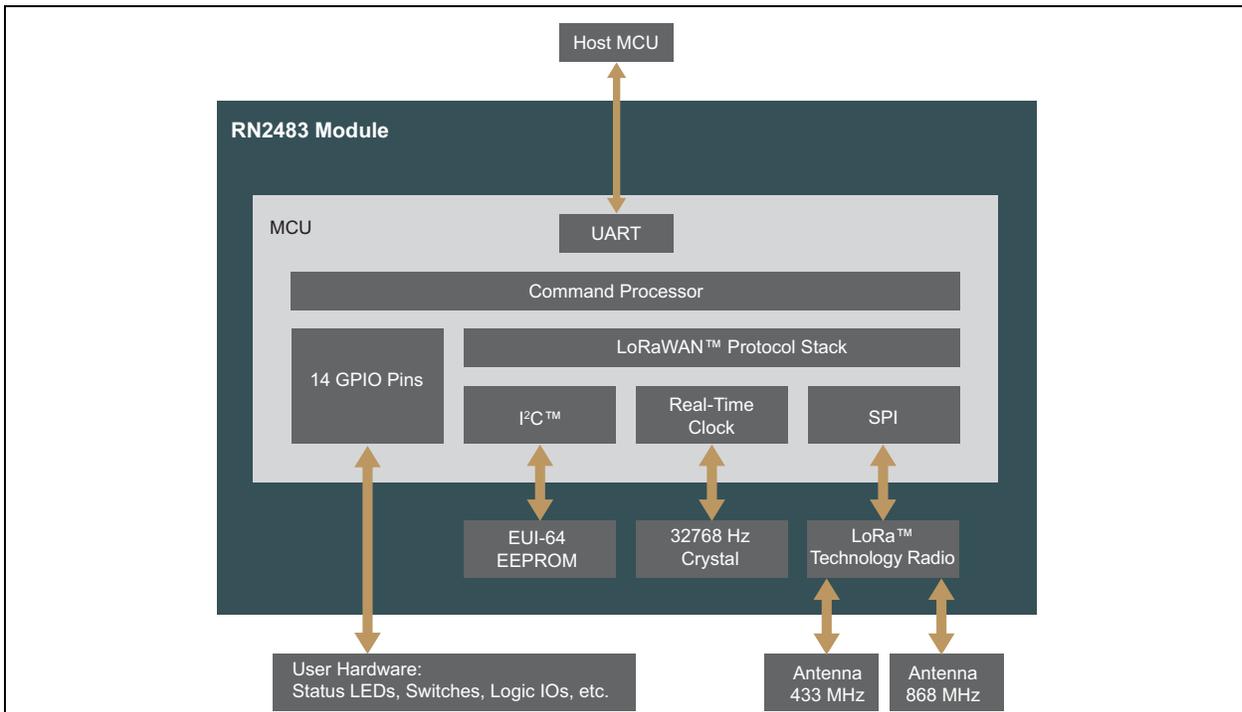


FIGURE 1-2: RN2483 PIN DIAGRAM

28	GND	27	GND	26	GND	25	RFI	24	GND	23	RFH	22	GND	21	GND	20	GND
29	NC															19	NC
30	TEST0															18	NC
31	TEST1															17	NC
32	RESET															16	NC
33	GND															15	NC
34	VDD															14	GPIO10
35	GPIO0															13	GPIO11
36	GPIO1															12	VDD
37	GPIO2															11	GND
38	GPIO3															10	GPIO12
39	GPIO4															9	GPIO13
40	GPIO5															8	GND
41	GND															7	UART_RX
42	NC															6	UART_TX
43	GPIO6															5	RESERVED
44	GPIO7															4	RESERVED
45	GPIO8															3	UART_CTS
46	GPIO9															2	UART_RTS
47	GND															1	GND

FIGURE 1-3: RN2483 BLOCK DIAGRAM



RN2483

Table 1-1 describes the module's pins.

TABLE 1-1: PIN DESCRIPTION

Pin	Symbol	Type	Description
1	GND	Power	Ground supply terminal
2	UART_RTS	Output	Communication UART RTS signal ⁽¹⁾
3	UART_CTS	Input	Communication UART CTS signal ⁽¹⁾
4	RESERVED	—	Do not connect
5	RESERVED	—	Do not connect
6	UART_TX	Output	Communication UART Transmit (TX)
7	UART_RX	Input	Communication UART Receive (RX)
8	GND	Power	Ground supply terminal
9	GPIO13	Input/Output	General purpose I/O pin
10	GPIO12	Input/Output	General purpose I/O pin
11	GND	Power	Ground supply terminal
12	VDD	Power	Positive supply terminal
13	GPIO11	Input/Output	General purpose I/O pin
14	GPIO10	Input/Output	General purpose I/O pin
15	NC	—	Not connected
16	NC	—	Not connected
17	NC	—	Not connected
18	NC	—	Not connected
19	NC	—	Not connected
20	GND	Power	Ground supply terminal
21	GND	Power	Ground supply terminal
22	GND	Power	Ground supply terminal
23	RFH	RF analog	RF signal pin for high band
24	GND	Power	Ground supply terminal
25	RFL	RF analog	RF signal pin for low band
26	GND	Power	Ground supply terminal
27	GND	Power	Ground supply terminal
28	GND	Power	Ground supply terminal
29	NC	—	Not connected
30	TEST0	—	Do not connect
31	TEST1	—	Do not connect
32	$\overline{\text{RESET}}$	Input	Active-low device Reset input
33	GND	Power	Ground supply terminal
34	VDD	Power	Positive supply terminal
35	GPIO0	Input/Output	General purpose I/O pin
36	GPIO1	Input/Output	General purpose I/O pin
37	GPIO2	Input/Output	General purpose I/O pin
38	GPIO3	Input/Output	General purpose I/O pin
39	GPIO4	Input/Output	General purpose I/O pin
40	GPIO5	Input/Output	General purpose I/O pin
41	GND	Power	Ground supply terminal
42	NC	—	Not connected
43	GPIO6	Input/Output	General purpose I/O pin

TABLE 1-1: PIN DESCRIPTION (CONTINUED)

Pin	Symbol	Type	Description
44	GPIO7	Input/Output	General purpose I/O pin
45	GPIO8	Input/Output	General purpose I/O pin
46	GPIO9	Input/Output	General purpose I/O pin
47	GND	Power	Ground supply terminal

Note 1: Optional handshake lines are supported in future firmware releases.

RN2483

2.0 GENERAL SPECIFICATIONS

Table 2-1 provides the general specifications for the module. Table 2-2 and Table 2-3 provide the module's electrical characteristics and current consumption. Table 2-4 and Table 2-5 show the module's dimensions and the RF output power calibration data.

TABLE 2-1: GENERAL SPECIFICATIONS

Specification	Description
Frequency Band	863.000 MHz to 870.000 MHz; 433.050 MHz to 434.790 MHz
Modulation Method	FSK, GFSK, and LoRa™ Technology modulation
Maximum Over-the-Air Data Rate	300 kbps with FSK modulation; 5468 bps with LoRa Technology modulation
RF connection	Board edge connection
Interface	UART
Operation Range	>15 km coverage at suburban; >5 km coverage at urban area
Sensitivity at 0.1% BER	-148 dBm ⁽¹⁾
RF TX Power	Adjustable up to max. 10 dBm on 433 MHz band (limited to meet regulations); max. 14 dBm on the 868 MHz band ⁽²⁾
Temperature (operating)	-40°C to +85°C
Temperature (storage)	-40°C to +115°C
Humidity	10% ~ 90% non-condensing

Note 1: Depends on modulation. Expand Spreading Factor (SF).

2: TX power is adjustable. For more information, refer to the “RN2483 LoRa™ Technology Module Command Reference User’s Guide” (DS40001784A).

TABLE 2-2: ELECTRICAL CHARACTERISTICS

Parameter	Min.	Typ.	Max.	Units
Supply Voltage	2.1	—	3.6	V
Voltage on any pin with respect to VSS (except VDD)	-0.3	—	VDD + 0.3	V
Voltage on VDD with respect to VSS	-0.3	—	3.9	V
Input Clamp Current (I _{IK}) (V _I < 0 or V _I > VDD)	—	—	+/-20	mA
Output Clamp Current (I _{OK}) (V _O < 0 or V _O > VDD)	—	—	+/-20	mA
GPIO sink/source current each	—	—	25/25	mA
Total GPIO sink/source current	—	—	200/185	mA
RAM Data Retention Voltage (in Sleep mode or Reset state)	1.5	—	—	V
VDD Start Voltage to ensure internal Power-on Reset signal	—	—	0.7	V
VDD Rise Rate to ensure internal Power-on Reset signal	0.05	—	—	V/ms
Brown-out Reset Voltage	1.75	1.9	2.05	V
Logic Input Low Voltage	—	—	0.15 x VDD	V
Logic Input High Voltage	0.8 x VDD	—	—	V
Input Leakage at <25°C (VSS < V _{PIN} < VDD, Pin at high-impedance)	—	0.1	50	nA
Input Leakage at +60°C (VSS < V _{PIN} < VDD, Pin at high-impedance)	—	0.7	100	nA
Input Leakage at +85°C (VSS < V _{PIN} < VDD, Pin at high-impedance)	—	4	200	nA
RF Input Level	—	—	+10	dBm

TABLE 2-3: CURRENT CONSUMPTION

Mode	Typical Current at 3V (mA)
Idle	2.8
RX	14.2
Deep Sleep	0.0099

TABLE 2-4: MODULE DIMENSIONS

Parameter	Value
Dimensions	17.8 x 26.7 x 3 mm
Weight	2.05g

TABLE 2-5: OUTPUT POWER OF TX POWER SETTING

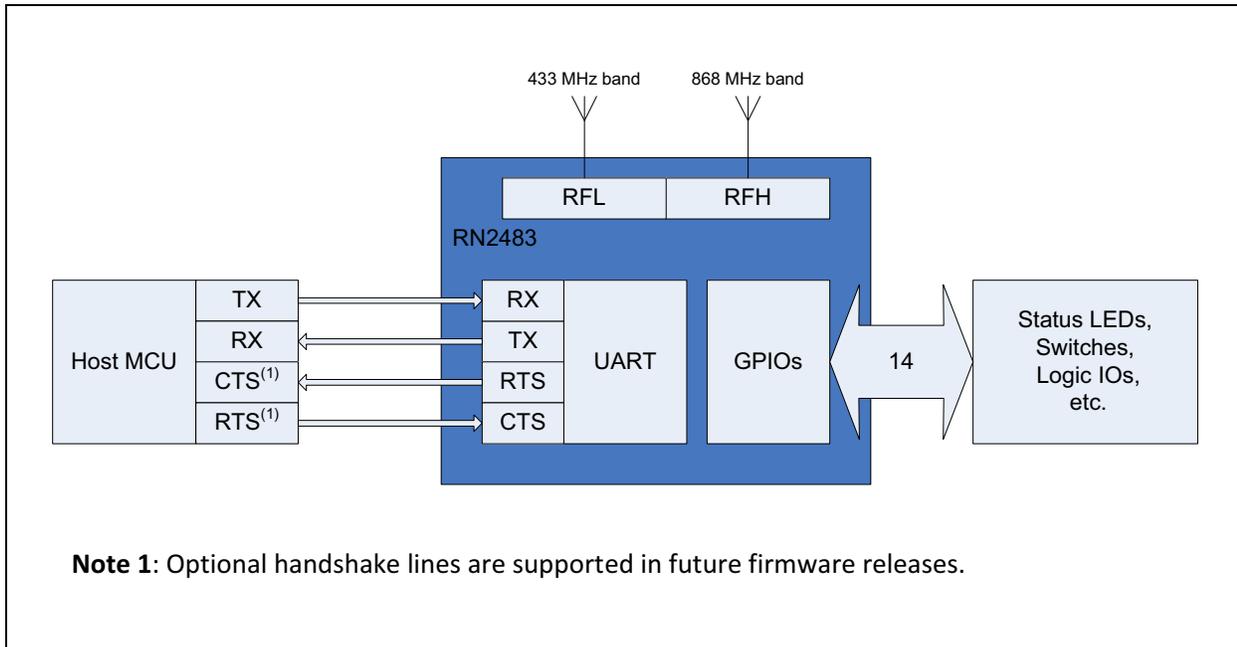
Band	TX Power Setting	Output Power (dBm)	Typical Supply Current at 3V (mA)
868 MHz	-3	-4.0	17.3
	-2	-2.9	18.0
	-1	-1.9	18.7
	0	-1.7	20.2
	1	-0.6	21.2
	2	0.4	22.3
	3	1.4	23.5
	4	2.5	24.7
	5	3.6	26.1
	6	4.7	27.5
	7	5.8	28.8
	8	6.9	30.0
	9	8.1	31.2
	10	9.3	32.4
	11	10.4	33.7
433 MHz	-3	-3.5	14.7
	-2	-2.3	15.1
	-1	-1.3	15.6
	0	-2.3	15.8
	1	-1.2	16.4
	2	-0.1	17.0
	3	1.0	17.7
	4	2.1	18.5
	5	3.2	19.4
	6	4.3	20.3
	7	5.4	21.4
	8	6.5	22.3
	9	7.6	23.3
	10	8.8	24.5
	11	9.9	25.8
12	10.9	27.3	
13	11.9	28.8	
14	12.9	30.7	
15	13.6	32.9	

RN2483

3.0 TYPICAL HARDWARE CONNECTIONS

Figure 3-1 shows the typical hardware connections.

FIGURE 3-1: HARDWARE CONNECTIONS



3.1 INTERFACE TO HOST MCU

The RN2483 module has a dedicated UART interface to communicate with a host controller. Optional handshake lines are supported in future firmware releases. The “RN2483 LoRa™ Technology Module Command Reference User’s Guide” (DS40001784A) provides a detailed UART command description. Table 3-1 shows the default settings for the UART communication.

TABLE 3-1: DEFAULT UART SETTINGS

Specification	Description
Baud Rate	57600 bps
Packet Length	8 bit
Parity Bit	No
Stop Bits	1 bit
Hardware Flow Control	No

3.2 GPIO PINS (GPIO0-GPIO13)

The module has 14 GPIO pins. These lines can be connected to switches, LEDs, and relay outputs. The pins are either logic inputs or outputs that can be accessed via the module firmware. These pins have limited sink and source capabilities. Electrical characteristics are described in Table 2-2.

3.3 RF CONNECTIONS (RFL, RFH)

RFL is the RF analog port for the lower frequency band (433 MHz) while RFH is for the higher frequency band (868 MHz). When routing RF paths, use proper strip lines with an impedance of 50 Ohm.

3.4 RESET PIN

The module’s reset pin is an active-low logic input.

3.5 POWER PINS

It is recommended to connect power pins (Pin 12 and 34) to a stable supply voltage with sufficient source current. Table 2-2 shows the current consumption.

Additional filtering capacitors are not required but can be used to ensure stable supply voltage in noisy environment.

4.0 PHYSICAL DIMENSIONS

Figure 4-1 and Figure 4-2 illustrate the physical dimensions and the recommended PCB layout for the RN2483 module.

FIGURE 4-1: RN2483 PHYSICAL DIMENSIONS

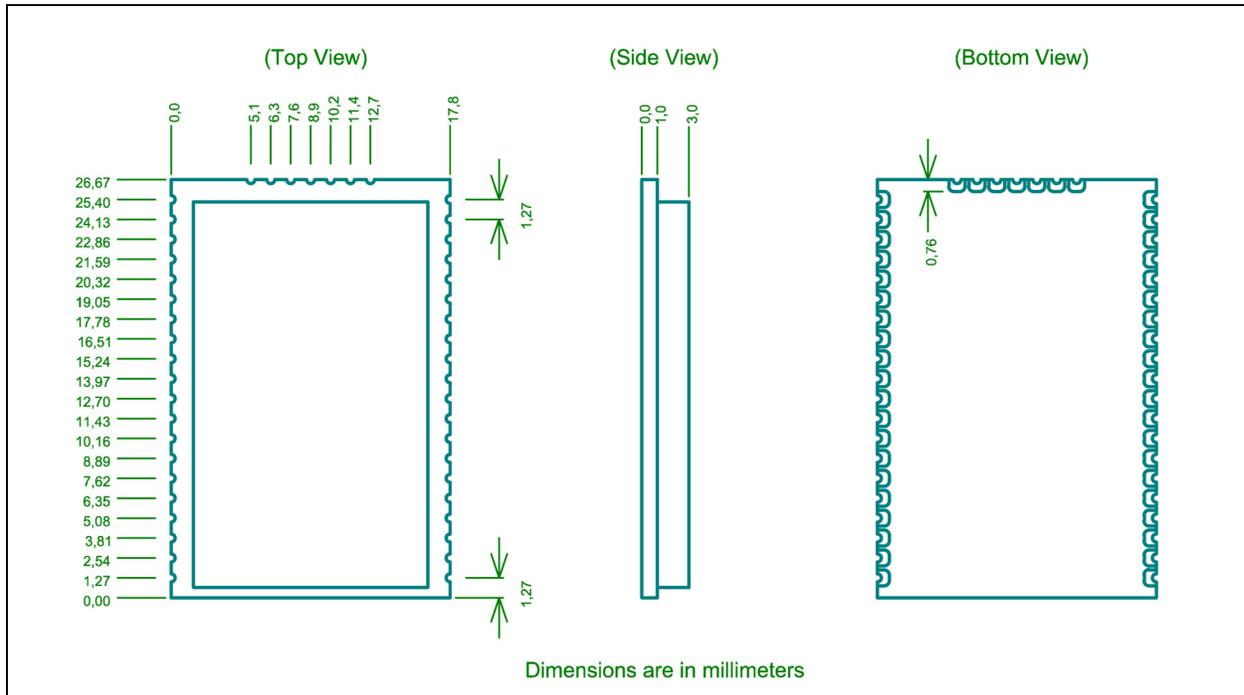
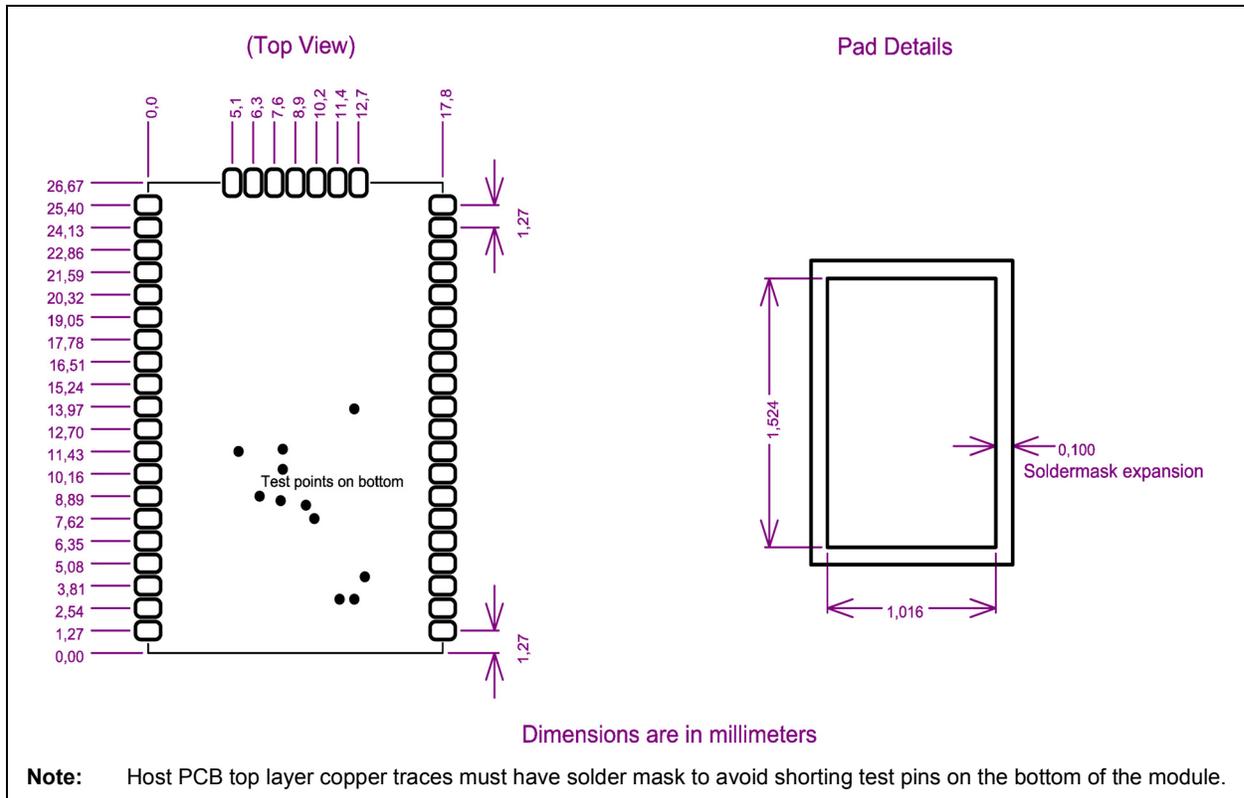


FIGURE 4-2: RECOMMENDED PCB FOOTPRINT

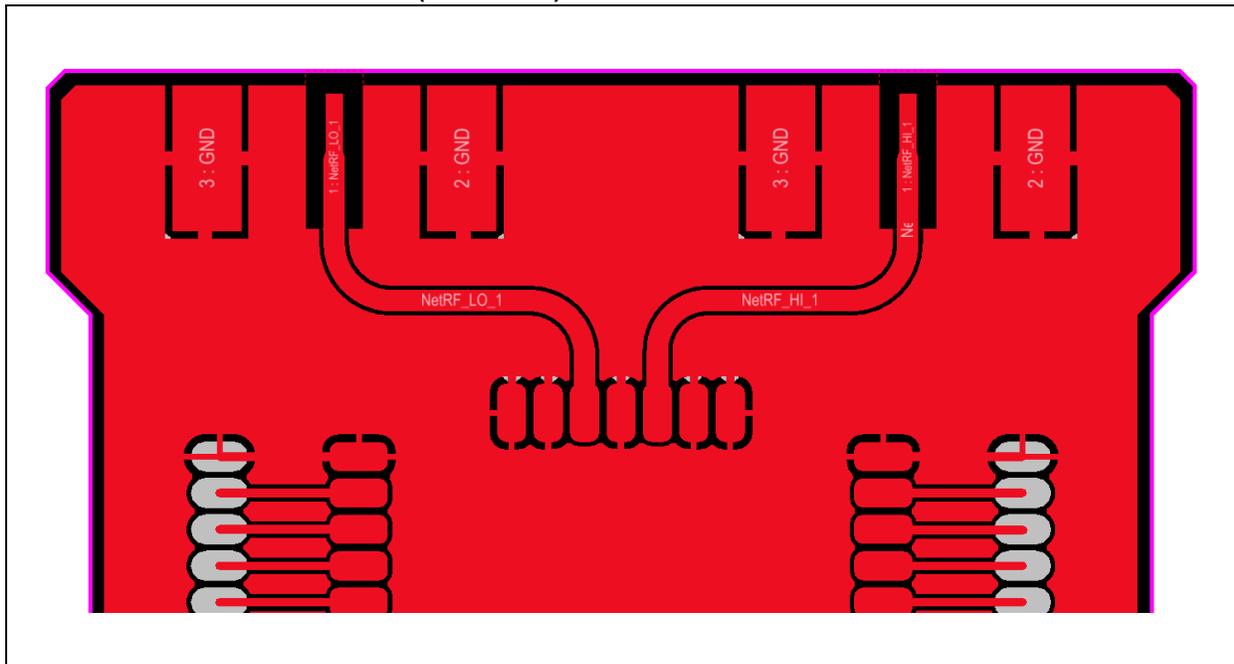


5.0 APPLICATION INFORMATION

5.1 RF pins and strip line

The RF signals must be routed with properly terminated 50 Ohm strip lines. Use curves instead of sharp corners. Keep the routing path as short as possible. When routing the RF paths, use proper strip lines with an impedance of 50 Ohm. [Figure 5-1](#) shows a routing example.

FIGURE 5-1: RF ROUTING (EXAMPLE)



6.0 REGULATORY APPROVAL

This section outlines the regulatory information for the RN2483 module for Europe.

6.1 Europe

The RN2483 module is an R&TTE Directive assessed radio module that is CE marked and has been manufactured and tested with the intention of being integrated into a final product.

The RN2483 module has been tested to R&TTE Directive 1999/5/EC Essential Requirements for Health and Safety (Article 3.1a), Electromagnetic Compatibility (EMC) (Article 3.1b), and Radio (Article 3.2) and are summarized in [Table 6-1: European Compliance Testing](#). A Notified Body Opinion has also been issued. All test reports are available on the product web page at <http://www.microchip.com>.

The R&TTE Compliance Association provides guidance on modular devices in document **Technical Guidance Note 01** available at http://www.rteca.com/html/download_area.htm.

Note: To maintain conformance to the testing listed in [Table 6-1: European Compliance Testing](#), the module shall be installed in accordance with the installation instructions in this data sheet and shall not be modified. When integrating a radio module into a completed product the integrator becomes the manufacturer of the final product and is therefore responsible for demonstrating compliance of the final product with the essential requirements of the R&TTE Directive.

6.1.1 LABELING AND USER INFORMATION REQUIREMENTS

The label on the final product which contains the RN2483 module must follow CE marking requirements. The “*R&TTE Compliance Association Technical Guidance Note 01*” provides guidance on final product CE marking.

TABLE 6-1: EUROPEAN COMPLIANCE TESTING

Certification	Standards	Article	Laboratory	Report Number	Date
Safety	IEC 60950-1:2005 (2nd Ed: A1:2009)	(3.1a)	TRaC Global Ltd.	TRA-025134-43-00A	2/12/2015
Health	EN 62479	—	TRaC Global Ltd.	TRA-025134-01-47-03A	2/12/2015
EMC	EN 301 489-3 v1.6.1	(3.1b)	TRaC Global Ltd.	TRA-025134-43-00A	2/12/2015
Radio	EN 300 220-2 v2.4.1	(3.2)	TRaC Global Ltd.	TRA-025134-01-47-00A (433 MHz) TRA-025134-01-47-01A(868MHz)	2/12/2015

6.1.2 EXTERNAL ANTENNA REQUIREMENTS

From R&TTE Compliance Association document **Technical Guidance Note 01**:

Provided the integrator installing an assessed radio module with an integral or specific antenna and installed in conformance with the radio module manufacturer's installation instructions requires no further evaluation under Article 3.2 of the R&TTE Directive and does not require further involvement of an R&TTE Directive Notified Body for the final product (Section 2.2.4).

6.1.3 HELPFUL WEB SITES

A document that can be used as a starting point in understanding the use of Short Range Devices (SRD) in Europe is the European Radio Communications Committee (ERC) Recommendation 70-03 E, which can be downloaded from the European Radio Communications Office (ERO) at: <http://www.ero.dk/>.

Additional helpful web sites are:

- Radio and Telecommunications Terminal Equipment (R&TTE): http://ec.europa.eu/enterprise/sectors/rtte/regulatory-framework/index_en.htm
- European Conference of Postal and Telecommunications Administrations (CEPT): <http://www.cept.org>
- European Telecommunications Standards Institute (ETSI): <http://www.etsi.org>
- European Radio Communications Office (ERO): <http://www.ero.dk/>
- The Radio and Telecommunications Terminal Equipment Compliance Association (R&TTE CA): <http://www.rteca.com/>

APPENDIX A: REVISION HISTORY

Revision A (March 2015)

This is the initial release of this document.

RN2483

NOTES:

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RN2483

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

<u>PART NO.</u>				
Device	Temperature Range	Package	Firmware Revision Number	
Device:	RN2483:	Low-Power Long Range LoRa™ Technology Transceiver module		
Temperature Range:	I	=	-40°C to +85°C (Industrial)	
Package:	RM	=	Radio Module	
				Examples: RN2483-I/RM: Industrial temperature

RN2483

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