Introduction to USB Type-C™

INTRODUCTION

The USB-IF has secured the ubiquitous nature of USB for years to come with the radically updated USB Type-C™ connector. While the sleek new reversible form factor has been significant for generating buzz and excitement from the general consumer market, the significantly expanded feature-set is what will eventually transform the desktop and entertainment environment.

The USB Type-C cable is now poised to become the “universal” cable, as it is capable of supplying blazing fast data transfer speeds of up to 10Gb/s, 100W of continuous power flow, and ultra high bandwidth video capabilities made available through Alternate Modes all in parallel with a single connection.

This document is intended for those already familiar with USB2.0/USB3.0/USB3.1 who are interested in the high level details of the expanded feature set that the USB Type-C cable brings to USB.

SECTIONS

Section 1.0, General Information
Section 2.0, USB Type-C Cables
Section 3.0, CC Pins
Section 4.0, VCONN Supply
Section 5.0, USB Power Delivery 2.0
Section 6.0, Alternate Modes

REFERENCES

This document is an introduction to USB Type-C™ and is not intended to be a replacement to the official specification. Consult the following specifications for technical details not described in this document.

- USB Type-C™ Specification
- USB Power Delivery 2.0 Specification
- USB 2.0 Specification
- USB 3.0 Specification
- USB 3.1 Specification
- USB Battery Charging BC1.2
1.0 GENERAL INFORMATION

The USB Type-C™ cable is a reversible 24-pin interconnect created by the USB-IF. The USB Type-C™ specification was first released in August 2014.

The USB Type-C cable is a universal cable that addresses the needs for a wide range of computing, display, and charging applications. The long-term objective of the USB Type-C cable is to replace all previous iterations of the USB cable while greatly expanding the overall capabilities. The recent introduction of the USB Power Delivery and Alternate Mode capabilities further expand the raw potential for even greater adoption of the USB standard in a wider range of applications.

FIGURE 1: USB CABLE PLUG FORM FACTORS

1.1 Port Behavior

Prior to the introduction of USB Type-C™ and USB Power Delivery, data and power roles were typically fixed. The shape of the receptacle/plug dictated both its data role and power role. USB Type-C connections are much more flexible; ports may be host-mode only, device-mode only, or dual-role and both the data and power roles can be independently and dynamically swapped using USB Power Delivery protocol. Because of this, there is some new terminology that is used to describe USB Type-C systems.

- **Downstream Facing Port (DFP)** - A host or downstream hub port. Typical of a legacy standard Type-A port.
- **Upstream Facing Port (UFP)** - A device or upstream hub port. Typical of a legacy standard Type-B port.
- **Dual-Role Port (DRP)** - A port that transitions between DFP and UFP port states until an attach event occurs. DRPs may be dynamically swapped using USB Power Delivery Protocol Negotiation after an initial attach event.
- **Power Source or Provider** - A source of 5V-20V up to 5A. Typical of a legacy standard Type-A port.
- **Power Sink or Consumer** - A sink of 5V-20V up to 5A. Typical of a legacy standard Type-B port.

1.2 Features

1.2.1 MINIMUM FEATURE SET

A basic USB Type-C application can still be cost-effective USB Type-C ports are **not** required to implement all of the advanced features that are defined in the specification. The minimum required feature set includes the following:

- USB2.0 Connection
- Cable attach and detach detection
- VCONN active cable supply
1.2.2 BATTERY CHARGING
While BC1.2 is still supported over USB Type-C because it depends on the USB2.0 lane, a significantly simplified and higher power current capability mechanism is also implemented. This simplified approach involves resistor pull-down/pull-up relationships. These pull-down/pull-up resistors are connected to the CC wire and the upstream facing port (UFP) must monitor the voltage on the CC1 and CC2 pins in order to detect the current sourcing capability of the downstream facing port (DFP) it is connected to. This is a substantial improvement over the complicated handshake mechanisms involved with USB BC1.2.

The basic USB Type-C current capabilities are Default USB (500mA for USB2.0 and 900mA for USB3.0), 1.5A@5V, and 3A@5V.

For additional details see Section 3.0, CC Pins.

1.2.3 USB2.0, USB3.0, USB3.1, AND BEYOND
The USB Type-C cable is designed to support current generation USB2.0 (480 Mb/s), USB3.0 (5Gb/s), USB3.1 (10Gb/s), and future USB specifications reaching up to 20Gb/s data rates.

For additional details see please refer to the individual specifications as published by the USB-IF.

1.2.4 POWER DELIVERY 2.0
USB Power Delivery protocol is a single-ended, 1-wire protocol created by the USB-IF which specifies the methods for serial communication over the USB Type-C CC wire. USB Power Delivery is required for implementation of the following advanced features:
- Communicating with an electronically marked/active cable
- Elevating the VBUS voltage above 5.5V
- Increasing current sourcing/sinking above 3A
- Changing default power roles (Provider or Consumer)
- Using Alternate Modes (see section 1.2.5)

The Power Delivery 2.0 is a port-to-port and port-to-cable communication protocol. The communication can not propagate throughout an entire device tree like standard USB protocols.

For additional details see Section 5.0, USB Power Delivery 2.0.

1.2.5 ALTERNATE MODES (THIRD PARTY PROTOCOLS)
The USB Type-C cable allows for any third party protocol to be used as long as the cable can support it. Alternate Modes are negotiated and entered on a port-to-port basis using the USB Power Delivery protocol. The following signals may be reassigned when entering an Alternate Mode.
- TX1+/-
- RX1+/-
- TX2+/-
- RX2+/-
- SBU1/SBU2

Separate specifications define the rules for each Alternate Mode. Currently, specifications exist for DisplayPort (authored by VESA) and ThunderBolt (authored by Intel). For additional details see Section 9.0, Alternate Modes.

1.3 Connector/Receptacle Pins

FIGURE 2: USB TYPE-C RECEPTACLE

![USB Type-C Receptacle Diagram](image-url)
The USB Type-C connector has 24 pins. Because of its reversibility, the pins are arranged in a mirrored configuration. There are a total of 6 differential pairs in a full-featured cable assembly. There are also 4 pins that serve functions new to USB: CC1, CC2, SBU1, SBU2.

1.3.1 USB2.0 DIFFERENTIAL PAIRS

The 2 sets of USB2.0 differential pairs in the connector pinout only connect to a single differential pair in standard USB2.0 or Full Featured USB Type-C cables. In a typical design, the D+ and D- pins are simply shorted on the PCB so that a multiplexer or switch is not required.

The second set of pins (B8/B7) may only be re-purposed in docking type applications where only 1 orientation is possible.

1.3.2 USB3.1 DIFFERENTIAL PAIRS

By default, only one set of TX/RX differential pairs are used for USB3.0/USB3.1 communication, depending on cable insertion orientation. Because of the cable reversibility, the USB3.0/USB3.1 lanes must be rerouted upon orientation connection. A typical application may use a 2:1 multiplexer to achieve this.

USB Power Delivery protocol and Alternate Modes allow some or all of the TX/RX differential pairs to be re-assigned.

1.3.3 CC1/CC2 PINS

The CC1 and CC2 pins are used to connect to the either the CC or VCONN wire in a USB Type-C cable. Both CC1 and CC2 pins must be able to support both CC and VCONN functions. The function is detected upon cable insertion.

The CC wire is used to cable orientation detection, USB Type-C current capability advertisement and detection, and USB2.0 BMC communication. See Section 3.0, CC Pins for additional details.

The VCONN wire is used to power active or electronically marked cables. See Section 4.0, VCONN Supply for additional details.

1.3.4 SBU1/SBU2

The SBU wires are lower speed signal wires that is allocated for Alternate Mode use only. USB Power Delivery is required for Alternate Mode negotiation before these pins may be used for any purpose.

TABLE 1: USB TYPE-C™ RECEPTACLE PINOUT

<table>
<thead>
<tr>
<th>Pin</th>
<th>Name</th>
<th>Function</th>
<th>Note</th>
</tr>
</thead>
<tbody>
<tr>
<td>A1</td>
<td>GND</td>
<td>Power</td>
<td>Support for 60W minimum (combined with all VBUS pins)</td>
</tr>
<tr>
<td>A2</td>
<td>TX1+</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with TX1-</td>
</tr>
<tr>
<td>A3</td>
<td>TX1-</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with TX1+</td>
</tr>
<tr>
<td>A4</td>
<td>VBUS</td>
<td>Power</td>
<td>Support for 60W minimum (combined with all VBUS pins)</td>
</tr>
<tr>
<td>A5</td>
<td>CC1</td>
<td>CC or VCONN</td>
<td>—</td>
</tr>
<tr>
<td>A6</td>
<td>D+</td>
<td>USB2.0</td>
<td>—</td>
</tr>
<tr>
<td>A7</td>
<td>D-</td>
<td>USB2.0</td>
<td>—</td>
</tr>
<tr>
<td>A8</td>
<td>SBU1</td>
<td>Alternate Mode</td>
<td>Lower speed side band signal</td>
</tr>
<tr>
<td>A9</td>
<td>VBUS</td>
<td>Power</td>
<td>Support for 60W minimum (combined with all VBUS pins)</td>
</tr>
<tr>
<td>A10</td>
<td>RX2-</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with RX2-</td>
</tr>
<tr>
<td>A11</td>
<td>RX2+</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with RX2-</td>
</tr>
<tr>
<td>A12</td>
<td>GND</td>
<td>Power</td>
<td>Support for 60W minimum (combined with all VBUS pins)</td>
</tr>
</tbody>
</table>
### TABLE 1:  USB TYPE-C™ RECEPTACLE PINOUT (CONTINUED)

<table>
<thead>
<tr>
<th>Pin</th>
<th>Name</th>
<th>Function</th>
<th>Note</th>
</tr>
</thead>
<tbody>
<tr>
<td>B1</td>
<td>GND</td>
<td>Power</td>
<td>Support for 60W minimum (combined with all VBUS pins)</td>
</tr>
<tr>
<td>B2</td>
<td>TX2+</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with TX2-</td>
</tr>
<tr>
<td>B3</td>
<td>TX2-</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with TX2+</td>
</tr>
<tr>
<td>B4</td>
<td>VBUS</td>
<td>Power</td>
<td>Support for 60W minimum (combined with all VBUS pins)</td>
</tr>
<tr>
<td>B5</td>
<td>CC2</td>
<td>CC or VCONN</td>
<td>—</td>
</tr>
<tr>
<td>B6</td>
<td>D+</td>
<td>USB2.0</td>
<td>—</td>
</tr>
<tr>
<td>B7</td>
<td>D-</td>
<td>USB2.0</td>
<td>—</td>
</tr>
<tr>
<td>B8</td>
<td>SBU2</td>
<td>Alternate Mode</td>
<td>Lower speed side band signal</td>
</tr>
<tr>
<td>B9</td>
<td>VBUS</td>
<td>Power</td>
<td>Support for 60W minimum</td>
</tr>
<tr>
<td>B10</td>
<td>RX1-</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with RX1+</td>
</tr>
<tr>
<td>B11</td>
<td>RX1+</td>
<td>USB3.1 or Alternate Mode</td>
<td>10Gb/s differential pair with RX1-</td>
</tr>
<tr>
<td>B12</td>
<td>GND</td>
<td>Power</td>
<td>Support for 60W minimum</td>
</tr>
</tbody>
</table>

### 1.4 Power Supply Options

The USB Type-C Interconnect introduces two new native charging options, but is also compatible with legacy charging options. USB Power Delivery is also supported but optional.

### TABLE 2:  USB TYPE-C™ POWER SUPPLY OPTIONS

<table>
<thead>
<tr>
<th>Mode</th>
<th>Nominal Voltage</th>
<th>Maximum Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>USB2.0</td>
<td>5V</td>
<td>500mA</td>
</tr>
<tr>
<td>USB3.0/USB3.1</td>
<td>5V</td>
<td>900mA</td>
</tr>
<tr>
<td>USB BC1.2</td>
<td>5V</td>
<td>1.5A</td>
</tr>
<tr>
<td>USB Type-C Current @ 1.5A</td>
<td>5V</td>
<td>1.5A</td>
</tr>
<tr>
<td>USB Type-C Current @ 2.0A</td>
<td>5V</td>
<td>3.0A</td>
</tr>
<tr>
<td>USB Power Delivery</td>
<td>Up to 20V</td>
<td>Up to 5A</td>
</tr>
</tbody>
</table>
2.0 USB TYPE-C CABLES

2.1 Physical Specifications

2.1.1 SIZE
The USB Type-C receptacle opening is 8.34mm x 2.56mm. For comparison, the Type-A receptacle opening is 12.50mm x 5.12mm while the USB3.0 micro-AB receptacle opening is 12.25mm x 1.85mm.

2.1.2 DURABILITY
The USB Type-C cable must minimally support 10,000 mating cycles.

2.1.3 WIRE GAUGE
Signal wire gauge is not explicitly specified in the USB Type-C™ specifications, but wires must be appropriately sized for the length and capabilities of the cable such that:

• Signal Integrity on the USB2.0 and USB3.0 wires is preserved
• ~50Ω impedance on the CC and SBU1/SBU2 wires
• Maximum IR drop of 250mV on GND return
• Maximum IR drop of 500mV on VBUS

2.1.4 CABLE LENGTH
Cable lengths are not explicitly specified in the USB Type-C™ specifications. However, the electrical requirements create some practical limits. USB3.1 Type-C to Type-C cable assemblies are allocated -6 dB loss at 5GHz, effectively limiting cable lengths to 1 meter. USB3.0 Type-C to Type-C cable assembly are allocated -7 dB loss at 5GHz, effectively limiting cable lengths to 2 meters.

<table>
<thead>
<tr>
<th>USB Version</th>
<th>Cable Length</th>
<th>Current Rating</th>
<th>USB</th>
<th>Electronically Marked</th>
</tr>
</thead>
<tbody>
<tr>
<td>USB2.0</td>
<td>≤ 4 meters</td>
<td>3A</td>
<td>Supported</td>
<td>Optional</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5A</td>
<td></td>
<td>Required</td>
</tr>
<tr>
<td>USB3.0</td>
<td>≤ 2 meters</td>
<td>3A</td>
<td>Supported</td>
<td>Optional</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5A</td>
<td></td>
<td>Required</td>
</tr>
<tr>
<td>USB3.1</td>
<td>≤ 1 meter</td>
<td>3A</td>
<td>Supported</td>
<td>Required</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5A</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

2.2 USB2.0
A standard USB2.0 Type-C cable assembly is shown in Figure 4 and Table 4.
TABLE 4: USB TYPE-C™ USB2.0 CABLE ASSEMBLY WIRING

<table>
<thead>
<tr>
<th>USB Type-C Plug 1</th>
<th>Wire</th>
<th>USB Type-C Plug 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pin Signal Name</td>
<td>Signal Name</td>
<td>Pin Signal Name</td>
</tr>
<tr>
<td>A1, B1, A12, B12</td>
<td>GND</td>
<td>1 GND_PWRRL1 [GND_PWRRL2]*</td>
</tr>
<tr>
<td>A4, B4, A9, B9</td>
<td>VBUS</td>
<td>2 PWR_VBUS1 [PWR_VBUS2]*</td>
</tr>
<tr>
<td>A5</td>
<td>CC</td>
<td>3 CC</td>
</tr>
<tr>
<td>B5</td>
<td>VCONN</td>
<td>[18] [PWR_VCONN]*</td>
</tr>
<tr>
<td>A6</td>
<td>DP</td>
<td>4 UTP_Dp</td>
</tr>
<tr>
<td>A7</td>
<td>DM</td>
<td>5 UTP_DM</td>
</tr>
<tr>
<td>Shell Shield</td>
<td>Shield</td>
<td>Braid</td>
</tr>
</tbody>
</table>

* Optional wires

2.3 Full Featured

A standard full-featured USB Type-C cable assembly is shown in Figure 5 and Table 5.
### Table 5: USB Type-C™ Full Featured Cable Assembly Wiring

<table>
<thead>
<tr>
<th>USB Type-C Plug 1</th>
<th>Wire</th>
<th>USB Type-C Plug 2</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Pin</strong></td>
<td><strong>Signal Name</strong></td>
<td><strong>Wire Number</strong></td>
</tr>
<tr>
<td>A1, B1, A12, B12</td>
<td>GND</td>
<td>1[16]**</td>
</tr>
<tr>
<td>A4, B4, A9, B9</td>
<td>VBUS</td>
<td>2[17]**</td>
</tr>
<tr>
<td>A5</td>
<td>CC</td>
<td>3</td>
</tr>
<tr>
<td>B5</td>
<td>VCONN</td>
<td>18</td>
</tr>
<tr>
<td>A6</td>
<td>DP</td>
<td>4</td>
</tr>
<tr>
<td>A7</td>
<td>DM</td>
<td>5</td>
</tr>
<tr>
<td>A2</td>
<td>SSTX1+</td>
<td>6</td>
</tr>
<tr>
<td>A3</td>
<td>SSTX1-</td>
<td>7</td>
</tr>
<tr>
<td>B11</td>
<td>SSRX1+</td>
<td>8</td>
</tr>
<tr>
<td>B10</td>
<td>SSRX1-</td>
<td>9</td>
</tr>
<tr>
<td>B2</td>
<td>SSTX2+</td>
<td>10</td>
</tr>
<tr>
<td>B3</td>
<td>SSTX2-</td>
<td>11</td>
</tr>
<tr>
<td>A11</td>
<td>SSRX2+</td>
<td>12</td>
</tr>
<tr>
<td>A10</td>
<td>SSRX2-</td>
<td>13</td>
</tr>
<tr>
<td>A8</td>
<td>SBU1</td>
<td>14</td>
</tr>
<tr>
<td>B8</td>
<td>SBU2</td>
<td>15</td>
</tr>
<tr>
<td>Shell</td>
<td>Shield</td>
<td>Braid</td>
</tr>
</tbody>
</table>

* Optional wires

#### 2.4 Passive Cables

A passive USB Type-C cable does not have embedded powered electronics. All passive cables must minimally support USB2.0, and it can support USB Power Delivery up to 60W of power.

#### 2.5 Powered Cable: Electronically Marked

An electronically marked cable has embedded electronics that can communicate with the USB ports via USB Power Delivery 2.0 BMC protocol. An electronically marked cable may be powered from the VCONN supply or directly from VBUS and may draw up to 70mW of total power.

*Use-case Example 1:* All USB3.1 compatible USB Type-C cables must be electronically marked.

*Use-case Example 2:* A 100W Power Delivery cable. Any cable capable of exceeding 60W of power carrying capability must be electronically marked and communicate is capabilities to the DFP port.

An electronically marked cable will behave identically to a standard passive cable if inserted into a receptacle that does not support USB Power Delivery 2.0.

#### 2.6 Powered Cable: Managed Active Cable

A managed active cable is an electronically marked cable that also has powered USB data reconditioning circuitry. A managed active cable may be powered from the VCONN supply or directly from VBUS and may draw up to 1.0W of total power.

*Use-case Example:* An active cable that uses repeaters/re-conditioners to extend the maximum cable length.

A managed active cable will behave identically to a standard active cable if inserted into a receptacle that does not support USB Power Delivery 2.0. It will still be able to power itself from VCONN or VBUS.
2.7 USB Type-C to Legacy USB Cables

The USB Type-C™ specification also defines the allowable USB Type-C to Legacy USB cable assemblies. The following full cable assemblies are supported:

- USB Type-C to Type-A (USB2.0)
- USB Type-C to Type-A (USB3.0/3.1)
- USB Type-C to Type-B (USB2.0)
- USB Type-C to Type-B (USB3.0/3.1)
- USB Type-C to Mini-B (USB2.0)
- USB Type-C to Micro-B (USB2.0)
- USB Type-C to Micro-B (USB3.0/3.1)

Only two USB Type-C to Legacy adapters are defined:

- USB Type-C to Type-A receptacle adapter
- USB Type-C to Micro-B (USB2.0)
3.0 CC PINS

The CC1 and CC2 pins are critical for basic USB Type-C operation. Resistors are attached to the CC pins in various configurations depending on whether the application is a downstream facing port (DFP), upstream facing port (UFP), or an electronically marked/active cable:

- Rp pull-up resistors on downstream facing ports (Section 3.1)
- Rd pull-down resistors on upstream facing ports (Section 3.2)
- Ra pull-down resistor on electronically marked/active cables (Section 3.3)

The CC1 and CC2 pins must be constantly monitored by the port to perform the following functions:

- Cable attach and removal detection (Section 3.4)
- Cable orientation detection (Section 3.5)
- Basic USB Type-C current capability advertisement (Section 3.6)

3.1 DFP Rp Pull-Up Resistors

The Rp pull-up resistors on a downstream facing port must be connected to both CC1 and CC2 pins, and may be pulled up to either 3.3V or 5.0V (a current source may also be used). The value of the resistor selected advertises the current supplying capability of the port to the device. The acceptable (per the USB Type-C™ specification) values for the Rp pull-up resistors and current sources are shown in the table below.

<table>
<thead>
<tr>
<th>DFP Current Capability</th>
<th>Resistor Pull-up to 4.75V - 5.5V</th>
<th>Resistor Pull-up to 3.3V ± 5%</th>
<th>Current Source to 1.7V - 5.5V</th>
</tr>
</thead>
<tbody>
<tr>
<td>Default USB Power (500mA for USB2.0, 900mA for USB3.0)</td>
<td>56 kΩ ± 20%</td>
<td>36 kΩ ± 20</td>
<td>80 μA ± 20%</td>
</tr>
<tr>
<td>1.5A @ 5V</td>
<td>22 kΩ ± 5%</td>
<td>12 kΩ ± 5%</td>
<td>180 μA ± 8%</td>
</tr>
<tr>
<td>3.0A @ 5V</td>
<td>10 kΩ ± 5%</td>
<td>4.7 kΩ ± 5%</td>
<td>330 μA ± 8%</td>
</tr>
</tbody>
</table>

3.2 UFP Rd Pull-Down Resistors.

An upstream facing port must connect a valid Rp pull-down resistor to GND (or optionally, a voltage clamp) to both CC1 and CC2 pins. A 5.1kΩ ± 10% is the only acceptable resistor if USB Type-C charging of 1.5A@5V or 3.0A@5V is to be used. The details are shown in the table below.

<table>
<thead>
<tr>
<th>Rd Implementation</th>
<th>Nominal Value</th>
<th>Detect Power Capability?</th>
<th>Current Source to 1.7V - 5.5V</th>
</tr>
</thead>
<tbody>
<tr>
<td>± 20% voltage clamp</td>
<td>1.1V</td>
<td>No</td>
<td>1.32V</td>
</tr>
<tr>
<td>± 20% resistor to GND</td>
<td>5.1kΩ</td>
<td>No</td>
<td>2.18V</td>
</tr>
<tr>
<td>± 10% resistor to GND</td>
<td>5.1kΩ</td>
<td>Yes</td>
<td>2.04V</td>
</tr>
</tbody>
</table>

3.3 Active Cable Ra Pull-Down Resistors

An active cable must connect an Ra resistor from the VCONN pin to GND. The Ra resistor may range from 800Ω to 1.2kΩ.

3.4 Cable Attach and Removal Detection

A cable attach is detected when either of the CC1 or CC2 pins detects a valid Rp/Rd connection. For a standard USB connection, only one of the CC1/CC2 pins may detect a valid Rp/Rd connection, not both.
5V to VBUS may only be applied when a valid cable attachment is detected. This prevents two downstream facing ports from back-driving current into each other.

### TABLE 8: CONNECTION STATES (FROM DFP PERSPECTIVE)

<table>
<thead>
<tr>
<th>CC1</th>
<th>CC2</th>
<th>State</th>
<th>Position</th>
</tr>
</thead>
<tbody>
<tr>
<td>Open</td>
<td>Open</td>
<td>Nothing Connected*</td>
<td>—</td>
</tr>
<tr>
<td>Rd</td>
<td>Open</td>
<td>UFP Connected</td>
<td>Unflipped</td>
</tr>
<tr>
<td>Open</td>
<td>Rd</td>
<td>UFP Connected</td>
<td>Flipped</td>
</tr>
<tr>
<td>Open</td>
<td>Ra</td>
<td>Powered Cable/No UFP connected</td>
<td>Unflipped</td>
</tr>
<tr>
<td>Ra</td>
<td>Open</td>
<td>Powered Cable/No UFP connected</td>
<td>Unflipped</td>
</tr>
<tr>
<td>Rd</td>
<td>Ra</td>
<td>Powered Cable/UFP connected</td>
<td>Unflipped</td>
</tr>
<tr>
<td>Ra</td>
<td>Rd</td>
<td>Powered Cable/UFP connected</td>
<td>Flipped</td>
</tr>
<tr>
<td>Rd</td>
<td>Rd</td>
<td>Debug Accessory Mode connected</td>
<td>—</td>
</tr>
<tr>
<td>Ra</td>
<td>Ra</td>
<td>Audio Adapter Mode connected</td>
<td>—</td>
</tr>
</tbody>
</table>

**Note:** *DFP-to-DFP and UFP-to-UFP are undetectable states.*

### 3.5 Cable Orientation Detection

The cable orientation is detected in the following way:

- If the CC1 pin detects a valid Rp/Rd connection, then the cable is in the “Unflipped” orientation at that receptacle.
- If the CC2 pin detects a valid Rp/Rd connection, then the cable is in the “Flipped” orientation at that receptacle.

### FIGURE 6: CABLE ORIENTATION DETECTION

[Diagram showing cable orientation detection]
3.6 USB Type-C Current Advertisement

Both the upstream facing port and the downstream facing port must monitor the voltage on the CC1 and CC2 pins to determine if a valid Rp/Rd or Rp/Ra connection has been made. The USB Type-C™ specification defines the following voltage ranges:

**TABLE 9: USB TYPE-C VOLTAGE RANGES**

<table>
<thead>
<tr>
<th>Current Advertisement</th>
<th>No Connection (Detached)</th>
<th>Rp / Rd Connection</th>
<th>Rp / Ra Connection</th>
</tr>
</thead>
<tbody>
<tr>
<td>3A</td>
<td>&gt;2.75V</td>
<td>2.60V - 0.85V</td>
<td>0.80V - 0.00V</td>
</tr>
<tr>
<td>1.5A</td>
<td>&gt;1.85V</td>
<td>1.60V - 0.45V</td>
<td>0.40V - 0.00V</td>
</tr>
<tr>
<td>Default USB (500mA/900mA)</td>
<td>&gt;1.85V</td>
<td>1.60V - 0.25V</td>
<td>0.20V - 0.00V</td>
</tr>
</tbody>
</table>

Once a valid connection is established, the upstream facing port (device) may is responsible for drawing the appropriate amount of maximum current.
4.0 VCONN SUPPLY

VCONN is a 5V (4.75V - 5.5V allowable range) 1.0W power supply used to power circuits within the plug that are needed to implement electronically marked cables and VCONN-powered accessories. The DFP is responsible for supplying VCONN by default. If two Dual-Role ports with USB Power Delivery support are connected to each other, the VCONN supplier can be swapped via USB PD negotiation.

VCONN is required for PD-enabled port and USB3 support. The VCONN power supply can be supplied in one of two ways:

a) If a valid Rp/Rd connection is detected on one of the CC pins, the VCONN supply can be blindly routed to the opposite CC pin.

b) After a valid Rp/Rd connection is detected on one of the CC pins, the opposite CC pin can be monitored for a valid Rp/Rd connection before routing the VCONN supply to the pin.

Because of the reversible nature of the USB Type-C cable, both CC1 and CC2 pins must be able to assume the role of CC and VCONN upon cable insertion. A typical solution is presented in fig xx below.

FIGURE 7: VCONN SUPPLY AND ACTIVE CABLE

Note: While all USB Type-C ports are required to source VCONN to active cables, active cables are permitted to source power from either VCONN or VBUS.
5.0 USB POWER DELIVERY 2.0

USB Power Delivery 2.0 refers to a single wire protocol (on CC wire) created by the USB-IF. The name “USB Power Delivery” can be somewhat misleading as it allows for much more than just power negotiations; it unlocks the advanced capabilities of the USB Type-C cable. The PD messaging occurs completely independently of USB2.0 or USB3.0/USB3.1 data and is used for port-to-port negotiation of power roles, voltage level, maximum supplying current capability, data roles, and Alternate Modes. Port-to-powered cable communication is also handled by USB PD.

5.1 Protocol Details

- All communication occurs over CC wire.
- The DFP is the Bus Master and initiates all communication.
- All messages are 32-bit 4b/6b encoded Bi-phase mark coded (BMC).
- 300k Baud rate
- CRC32 error detection + message retries
- Terminology:
  - SOP: DFP to DFP messaging
  - SOP*: DFP to active cable plug messaging
  - SOP**: DFP to active cable plug messaging

**FIGURE 8: SOP SIGNALING**

![SOP SIGNALING Diagram]

Note: SOP’ is assigned to one plug of the cable while SOP” is assigned to the other. The cable plugs cannot tell which side they are connected to, just that one end may respond to SOP’ addressed messages and the other may respond SOP” addressed messages.

5.2 Power Delivery Negotiation

USB Power Delivery allows power configuration of a USB connection to be dynamically modified. The default 5V voltage on VBUS can be reconfigured up to any level up to 20V. The maximum current supplying capability can also be raised to a maximum of 5A with a 100W compatible electronically marked USB PD Type-C cable.

The default roles (Provider or Consumer) can also be dynamically swapped at any time if both ports support dual power role functionality and the port accepts the swap request.

5.3 Alternate Mode and Data Role Negotiation.

Alternate Modes allow third party protocols to be transmitted over the USB Type-C cable. They are negotiated on port-to-port basis with Power Delivery protocol. See Section 6.0, Alternate Modes for more information.

Data roles can also be swapped dynamically over USB PD protocol negotiation.
5.4 Billboard Device

Because of the wide range of capabilities enabled with USB PD, it can become confusing for the end user. There may be times when a user connects two devices and expects a different result than what actually occurs. To provide some amount of feedback to the user, a USB 2.0 "Billboard" class device connected to the Power Delivery system can provide messages to the user that can explain errors or compatibility issues.
6.0 ALTERNATE MODES

Alternate Modes and USB Power Delivery are the two key features that will allow the USB Type-C cable to become a true “universal” cable. Alternate Modes allow the USB Type-C cable to be reconfigured to support third party protocols. This feature is enabled only if both ports support the USB Power Delivery protocol and are both compatible with the specific Alternate Mode.

There are no specific limits on Alternate Modes. As long as the cable can support the third party protocol signaling while maintaining a USB2.0 connection, then the Alternate Mode can be implemented. The USB Type-C™ specification does not define any Alternate Modes; each third party must maintain its own USB Type-C Alternate Mode specification.

Alternate Mode negotiation is performed via USB Power Delivery protocol on a port-to-port basis.

6.1 Reconfigurable Pins

All Alternate Modes must minimally maintain a USB2.0 and USB Power Delivery connection. The following pins/wires may be reconfigured for the use with the Alternate Mode.

FIGURE 9: RECONFIGURABLE PINS ON A FULL FEATURED CABLE

FIGURE 10: RECONFIGURABLE PINS ON A DIRECT CONNECT APPLICATION

6.2 Example: DisplayPort

DisplayPort was one of the first 3rd part protocols to be specified as a USB Type-C™ Alternate Mode. The DisplayPort Alternate mode supports the following modes of operation:

• (2) Display Port lanes + (1) USB3.1 lane
• (4) Display Port lanes

FIGURE 11: (2) DISPLAY PORT LANES + (1) USB3.1 LANE EXAMPLE
APPENDIX A: APPLICATION NOTE REVISION HISTORY

TABLE A-1: REVISION HISTORY

<table>
<thead>
<tr>
<th>Revision Level &amp; Date</th>
<th>Section/Figure/Entry</th>
<th>Correction</th>
</tr>
</thead>
<tbody>
<tr>
<td>A (2-9-15)</td>
<td>Unfinished Pre-Release</td>
<td></td>
</tr>
</tbody>
</table>
THE MICROCHIP WEB SITE

Microchip provides online support via our WWW site at www.microchip.com. This web site is used as a means to make files and information easily available to customers. Accessible by using your favorite Internet browser, the web site contains the following information:

- **Product Support** – Data sheets and errata, application notes and sample programs, design resources, user’s guides and hardware support documents, latest software releases and archived software
- **General Technical Support** – Frequently Asked Questions (FAQ), technical support requests, online discussion groups, Microchip consultant program member listing
- **Business of Microchip** – Product selector and ordering guides, latest Microchip press releases, listing of seminars and events, listings of Microchip sales offices, distributors and factory representatives

CUSTOMER CHANGE NOTIFICATION SERVICE

Microchip’s customer notification service helps keep customers current on Microchip products. Subscribers will receive e-mail notification whenever there are changes, updates, revisions or errata related to a specified product family or development tool of interest.

To register, access the Microchip web site at www.microchip.com. Under “Support”, click on “Customer Change Notification” and follow the registration instructions.

CUSTOMER SUPPORT

Users of Microchip products can receive assistance through several channels:

- Distributor or Representative
- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support

Customers should contact their distributor, representative or field application engineer (FAE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the web site at: http://microchip.com/support
Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip’s Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as “unbreakable.”

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE, MICROCHIP DISCLAIMS ALL LIABILITY ARISING FROM THIS INFORMATION AND ITS USE. USE OF MICROCHIP DEVICES AS CRITICAL COMPONENTS IN LIFE SUPPORT AND/OR SAFETY APPLICATIONS IS ENTIRELY AT THE BUYER'S RISK, AND THE BUYER AGREES TO DEFEND, INDEMNIFY AND HOLD HARMLESS MICROCHIP FROM ANY AND ALL DAMAGES, CLAIMS, SUITS, OR EXPENSES RESULTING FROM SUCH USE. NO LICENSES ARE CONVEYED, IMPLICITLY OR OTHERWISE, UNDER ANY MICROCHIP INTELLECTUAL PROPERTY RIGHTS.

Trademarks

The Microchip name and logo, dsPIC, FlashFlex, KEELOG, KEELQ, logo, MPLAB, PIC, PICmicro, PICSTART, PIC32 logo, rFIC, SST, SST Logo, SuperFlash and UNI/O are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

FilterLab, Hampshire, Hi-TECH C, Linear Active Thermistor, MTP, SEEVAL and The Embedded Control Solutions Company are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Silicon Storage Technology is a registered trademark of Microchip Technology Inc. in other countries.

Analog-for-the-Digital Age, Application Maestro, BodyCom, chipKIT, chipKIT logo, CodeGuard, dsPICDEM, dsPICDEM.net, dsPICworks, dsSPEAK, ECONOMONITOR, FanSense, HI-TIDE, In-Circuit Serial Programming, ICSP, MIRI, MIRI, MPASM, MPF, MPLAB Certified logo, MPLIB, MPLINK, mTouch, Omniscient Code Generation, PICC, PICC-18, PICDEM, PICDEM.net, PICkit, PICtail, REAL ICE, rFIC, Select Mode, SQI, Serial Quad I/O, Total Endurance, TSHARC, UniWinDriver, WiperLock, ZENA and Z- Scale are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

GestIC and ULPP are registered trademarks of Microchip Technology Germany II GmbH & Co. KG, a subsidiary of Microchip Technology Inc., in other countries.

A more complete list of registered trademarks and common law trademarks owned by Standard Microsystems Corporation (“SMSC”) is available at: www.smcs.com. The absence of a trademark (name, logo, etc.) from the list does not constitute a waiver of any intellectual property rights that SMSC has established in any of its trademarks.

All other trademarks mentioned herein are property of their respective companies.

© 2015, Microchip Technology Incorporated, Printed in the U.S.A., All Rights Reserved.

ISBN: 978-1-63277-475-0

QUALITY MANAGEMENT SYSTEM
CERTIFIED BY DNV
★ ISO/TS 16949 ★
### Worldwide Sales and Service

#### AMERICAS
**Corporate Office**
2350 West Chandler Blvd.
Chandler, AZ 85224-0199
Tel: 480-792-7200
Fax: 480-792-7277
Technical Support: http://www.microchip.com/support
Web Address: www.microchip.com

**Atlanta**
Duluth, GA
Tel: 678-957-9814
Fax: 678-957-1455

**Austin, TX**
Tel: 512-257-3370

**Boston**
Westborough, MA
Tel: 774-760-0087
Fax: 774-760-0086

**Chicago**
Itasca, IL
Tel: 630-285-0071
Fax: 630-285-0075

**Cleveland**
Independence, OH
Tel: 216-447-0464
Fax: 216-447-0643

**Dallas**
Addison, TX
Tel: 972-818-7423
Fax: 972-816-2924

**Detroit**
Novi, MI
Tel: 248-848-4000

**Houston, TX**
Tel: 281-894-5983

**Indianapolis**
Noblesville, IN
Tel: 317-773-8323
Fax: 317-773-5453

**Los Angeles**
Mission Viejo, CA
Tel: 949-462-9523
Fax: 949-462-9606

**New York, NY**
Tel: 631-435-6000

**San Jose, CA**
Tel: 408-735-9110

**Canada - Toronto**
Tel: 905-673-6699
Fax: 905-673-8500

#### ASIA/PACIFIC
**Asia Pacific Office**
Suites 3707-14, 37th Floor
Tower 6, The Gateway Harbour City, Kowloon
Hong Kong
Tel: 852-2943-5100
Fax: 852-2401-3431

**Australia - Sydney**
Tel: 61-2-9868-8733
Fax: 61-2-8806-6765

**China - Beijing**
Tel: 86-10-8569-7000
Fax: 86-10-6528-2104

**China - Chengdu**
Tel: 86-28-8665-5511
Fax: 86-28-8668-7889

**China - Chongqing**
Tel: 86-23-8900-9988
Fax: 86-23-8900-9900

**China - Hangzhou**
Tel: 86-571-8792-8115
Fax: 86-571-8792-8116

**China - Hong Kong SAR**
Tel: 852-2943-5100
Fax: 852-2401-3431

**China - Nanjing**
Tel: 86-25-8473-2460
Fax: 86-25-8473-2470

**China - Qingdao**
Tel: 86-532-8502-7355
Fax: 86-532-8502-7205

**China - Shanghai**
Tel: 86-21-5407-5533
Fax: 86-21-5407-5006

**China - Shenyang**
Tel: 86-24-2334-2829
Fax: 86-24-2334-2933

**China - Shenzhen**
Tel: 86-755-8864-2200
Fax: 86-755-8203-1760

**China - Wuhan**
Tel: 86-27-5980-5300
Fax: 86-27-5980-5118

**China - Xian**
Tel: 86-29-8833-7252
Fax: 86-29-8833-7256

**China - Xiamen**
Tel: 86-592-2398138
Fax: 86-592-2398130

**China - Zhuhai**
Tel: 86-756-3210040
Fax: 86-756-3210049

#### ASIA/PACIFIC
**India - Bangalore**
Tel: 91-80-3090-4444
Fax: 91-80-3090-4123

**India - New Delhi**
Tel: 91-11-4150-8831
Fax: 91-11-4150-8832

**India - Pune**
Tel: 91-20-3019-1500

**Japan - Osaka**
Tel: 81-6-6152-7100
Fax: 81-6-6152-9310

**Japan - Tokyo**
Tel: 81-3-9880-3770
Fax: 81-3-9880-3771

**Korea - Daegu**
Tel: 82-53-744-4301
Fax: 82-53-744-4302

**Korea - Seoul**
Tel: 82-2-554-7200
Fax: 82-2-558-5932 or 82-2-558-5934

**Malaysia - Kuala Lumpur**
Tel: 60-3-6201-9857
Fax: 60-3-6201-9850

**Malaysia - Penang**
Tel: 60-4-227-8870
Fax: 60-4-227-8868

**Philippines - Manila**
Tel: 63-2-634-9065
Fax: 63-2-634-9069

**Singapore**
Tel: 65-6334-8870
Fax: 65-6334-8865

**Taiwan - Hsin Chu**
Tel: 886-3-5776-9666
Fax: 886-9-5776-9556

**Taiwan - Kaohsiung**
Tel: 886-7-213-7030

**Taiwan - Taipei**
Tel: 886-2-2586-8000
Fax: 886-2-2586-0102

**Thailand - Bangkok**
Tel: 66-2-694-1351
Fax: 66-2-694-1350

#### EUROPE
**Austria - Wels**
Tel: 43-7242-2244-39
Fax: 43-7242-2244-393

**Denmark - Copenhagen**
Tel: 45-4450-2628
Fax: 45-4465-2629

**France - Paris**
Tel: 33-1-69-53-63-20
Fax: 33-1-69-30-90-79

**Germany - Dusseldorf**
Tel: 49-2120-3766400

**Germany - Munich**
Tel: 49-89-627-144-0
Fax: 49-89-627-144-44

**Germany - Pforzheim**
Tel: 49-7231-424750

**Italy - Milan**
Tel: 39-0331-742611
Fax: 39-0331-688781

**Italy - Venice**
Tel: 39-049-7625266

**Netherlands - Drunen**
Tel: 31-416-060399
Fax: 31-416-060340

**Poland - Warsaw**
Tel: 48-22-3325737

**Spain - Madrid**
Tel: 34-91-708-08-90
Fax: 34-91-708-08-91

**Sweden - Stockholm**
Tel: 46-8-5000-4654

**UK - Wokingham**
Tel: 44-118-921-5800
Fax: 44-118-921-5820

01/30/15