Power

7-Bit Unshifted I2C Address
Open Jumper (Default): 0x37
Closed Jumper: 0x36
Software configurable as well

I2C Pull Ups

Button Triggers

3.3V VReg prevents USB 5V from being injected onto Qwiic 3.3V

µSD

MP3

AVR

Amp

Connections

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TITLE: Qwiic MP3 Trigger

Design by: N. Seidle

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Sheet: 1/1