Interface and Logic Conversion

VCC must be 3.7-5.5V

Internal DC-DC will use the following:
- 900mA @ 5V
- 1100mA @ 3.7V

RFID Module

SJ1 and SJ2 select between PCB antenna and the u.FL connector. Default is PCB antenna.

Note: This device operates in the 860MHz and 920MHz bands. Other devices operating near these bands will be affected and affect the performance of the reader.


Hardware

Serial

Software

Serial

External Power

Shield and Serial Select

Changes on v10:
- Decreased size of RF pad to avoid GND pad on module
- Finalized trace antenna design and matching network
- Added thermals to user solderable GND connections
- Opened up heatsink area on bottom of PCB

Caution: Module gets hot when reading at full power

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Caution: Module gets hot when reading at full power

Internal DC-DC will use the following:
- 900mA @ 5V
- 1100mA @ 3.7V

These 3 GPIO-LV pins are 3.3V tolerant. Do not expose them to 5V.

OPEN JP1 if powering from a source that you do not want feeding back to USB

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