**I2C Address Table**

<table>
<thead>
<tr>
<th>SDOM/AG</th>
<th>AG Addr.</th>
<th>M Addr.</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0x6A</td>
<td>0x1C</td>
</tr>
<tr>
<td>1</td>
<td>0x6B</td>
<td>0x1E</td>
</tr>
</tbody>
</table>

**Jumpers**

SJ1 pulls up the chip-select (CS) pins. Closed: Sets LSM9DS1 to I2C mode. Open: Allows for CS control in SPI mode.

SJ2 pulls up the serial data out (SDO) pins. Closed: Sets I2C addresses in I2C mode. Open: Floats SDO pins - can be used to set I2C address, or be used for SPI control.

SJ3 pulls up the SCL and SDA pins. Closed: Adds 10k pull-ups to I2C lines. Open: Removes 10k pull-ups. SPI mode or external I2C pull-ups can be used.

**Headers**

**VDD Range:** 1.9-3.6V

**Release**

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**Title:** SparkFun-LSM9DS1-Breakout_v10

**Design by:** Jim Lindblom

**Date:** 3/5/2015 3:55:14 PM

**Sheet:** 1/1