



\* $2.375V < VCC < 3.465V$

\*The LSB bit of the 7 bit address is determined by the logic level on pin AD0.  
Default Address = 0x68 (pin AD0 is logic low)  
Alternative Address = 0x69 (pin AD0 is logic high)

\*\*Optional external reference clock input or synchronization digital input.  
Connected to GND by default. Cut trace for external control



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TITLE: mpu-9150\_breakout

Design by:

REV:

Date: 5/13/2014 11:39:26 AM

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